

SILICON POWER TRANSISTOR 2SD560

NPN SILICON EPITAXIAL TRANSISTOR (DARLINGTON CONNECTION) FOR LOW-FREQUENCY POWER AMPLIFIERS AND LOW-SPEED SWITCHING

The 2SD560 is a mold power transistor developed for lowfrequency power amplifiers and low-speed switching. This transistor is ideal for direct driving from the IC output of devices such as pulse motor drivers and relay drivers, and PC terminals.

ORDERING INFORMATION

| Ordering Name | Package |
|---------------|----------|
| 2SD560 | TO-220AB |

(TO-220AB)

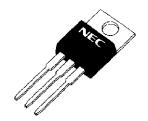


FEATURES

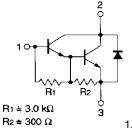
- · C-to-E reverse diode inserted
- · Low collector saturation voltage

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

| Parameter | Symbol | Conditions | Ratings | Unit |
|------------------------------|--------------------|-----------------------|-------------|------|
| Collector to base voltage | Vсво | | 150 | V |
| Collector to emitter voltage | VCEO | | 100 | ٧ |
| Emitter to base voltage | VEBO | | 7.0 | ٧ |
| Collector current (DC) | Ic(DC) | | ±5.0 | Α |
| Collector current (pulse) | IC(pulse) | PW ≤ 10 ms, | ±8.0 | Α |
| | | duty cycle ≤ 50% | | |
| Base current (DC) | I _{B(DC)} | | 0.5 | Α |
| Total power dissipation | Р⊤ | Tc = 25°C | 30 | W |
| | | T _A = 25°C | 1.5 | W |
| Junction temperature | Tj | | 150 | °C |
| Storage temperature | T _{stg} | | -55 to +150 | ô |



INTERNAL EQUIVALENT CIRCUIT



- 1. Base
- 2. Collector
- 3. Emitter

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Not all devices/types available in every country. Please check with local NEC representative for availability and additional information.

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ELECTRICAL CHARACTERISTICS (TA = 25°C)

| Parameter | Symbol | Conditions | MIN. | TYP. | MAX. | Unit |
|------------------------------|----------------------|---|-------|-------|--------|------|
| Collector cutoff current | Ісво | Vcb = 100 V, IE = 0 A | | | 1.0 | μΑ |
| DC current gain | h _{FE1} | $V_{\text{CE}} = 2.0 \text{ V}, \text{ Ic} = 3.0 \text{ A}^{\text{Note}}$ | 2,000 | 6,000 | 15,000 | |
| | h _{FE2} | VcE = 2.0 V, Ic = 5.0 A ^{Note} | 500 | | | |
| Collector saturation voltage | V _{CE(sat)} | Ic = 3.0 A, I _B = 3.0 mA ^{Note} | | 0.9 | 1.5 | V |
| Base saturation voltage | V _{BE(sat)} | Ic = 3.0 A, I _B = 3.0 mA ^{Note} | | 1.6 | 2.0 | V |
| Turn-on time | ton | $Ic = 3.0 \text{ A}, R_L = 16.7 \Omega,$ | | 1.0 | | μs |
| Storage time | t stg | l _{B1} = −l _{B2} = 3.0 mA, V _{CC} ≅ 50 V Refer to the test circuit. | | 3.5 | | μs |
| Fall time | tf | | | 1.2 | | μs |

Note Pulse test PW \leq 350 μ s, duty cycle \leq 2%

hfe CLASSIFICATION

| Marking | MB | LB | КВ |
|------------------|----------------|----------------|-----------------|
| h _{FE1} | 2,000 to 5,000 | 3,000 to 7,000 | 5,000 to 15,000 |

SWITCHING TIME (ton, tstg, tf) TEST CIRCUIT

Base current Collector current PW waveform PW ⇒ 50 μ s $V_{BB} \doteq -5.0~V$ Duty Cycle ≤ 2%

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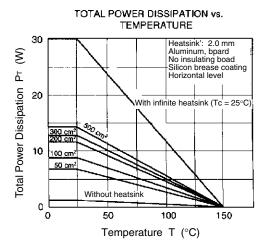
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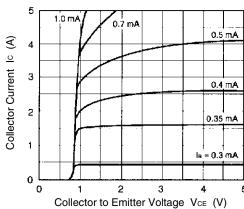
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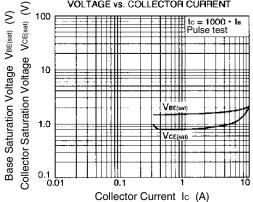
TYPICAL CHARACTERISTICS (TA = 25°C)



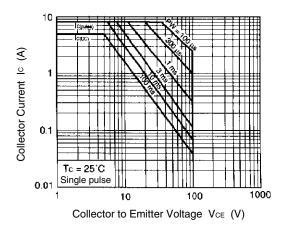




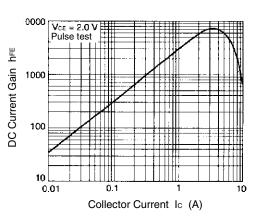
BASE AND COLLECTOR SATURATION VOLTAGE vs. COLLECTOR CURRENT



FORWARD BIAS SAFE OPERATING AREA



DC CURRENT GAIN vs. COLLECTOR CURRENT



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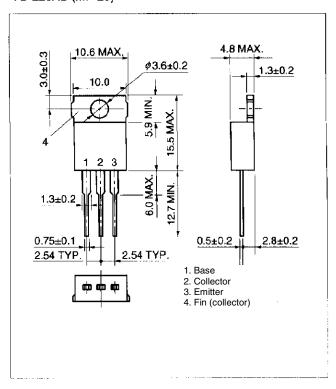
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PACKAGE DRAWING (UNIT: mm)

TO-220AB (MP-25)



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